

Hardware Scheduler Memory Arrangement

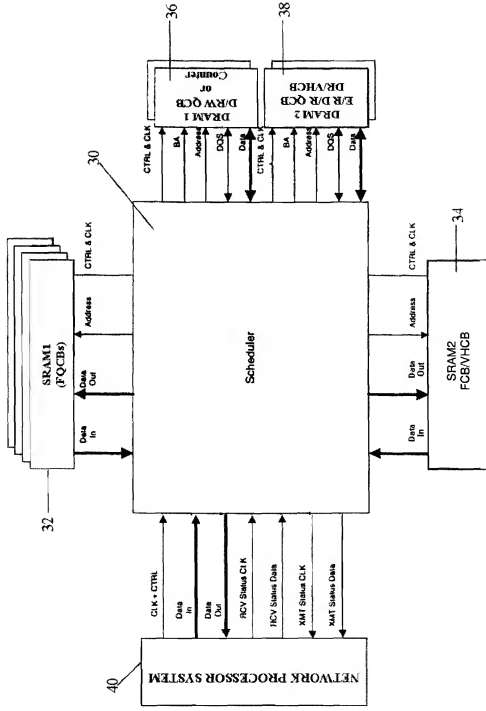


Figure 1

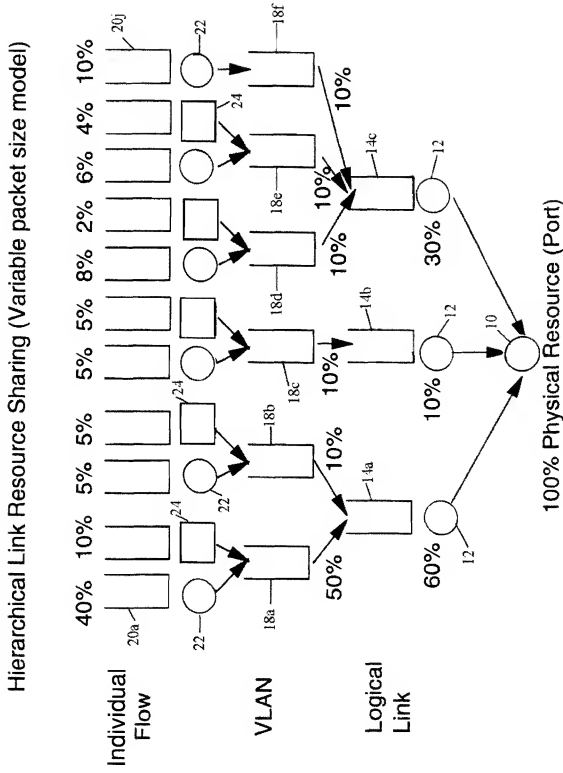


Figure 2